Applicants: Hakan OZDEMIR Application No. 09-993877
Filing Date: November 5, 2010 Attorney Docket No.:1678-022-01

I. AMENDMENT

In the Specification:

Please amend page 16 line 29 as below:

above. After a first predetermined delay, which is <u>between one and ten clock cycles</u> in one embodiment, the processor **40** causes the sample-interpolator loop **54** and gain circuit **38** to enter the